

# Model Reduction in the Time-Domain using Laguerre Polynomials and Krylov Methods \*

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## Abstract

We present a new passive model reduction algorithm based on the Laguerre expansion of the time response of interconnect networks. We derive expressions for the Laguerre coefficient matrices that minimize a weighted square of the approximation error, and show how these matrices can be computed efficiently using Krylov subspace methods. We discuss the connections between our method and other methods such as PRIMA [4]. Numerical simulations show that our method can better approximate the original model as compared to PRIMA.

## 1 Introduction

With the increasing complexity of VLSI circuits, the models of interconnect networks have become so large that their analysis using brute-force techniques has become impractical. A popular approach to solve this problem is model reduction, where an approximate small-scale model is used in place of the original large-scale system.

A number of model-reduction methods are available in the literature. Moment matching techniques [1, 9, 4] can be used to generate passive reduced order models for interconnects. More sophisticated methods such as balance-and-truncate model reduction have also been proposed for interconnect modeling [5, 3, 6]. Krylov methods such as the Arnoldi and Lanczos iterations apply naturally in the numerical implementation of these techniques. All of these methods focus on approximating the frequency response of the original system and are hence referred to as frequency-domain methods.

A natural counterpart to frequency-domain methods are time-domain methods where the focus is on directly approximating the time response of the system (it can be argued that this is a more natural approach, as typically it is the time response that is of interest). Krylov methods have been applied in this context as well; see [2], where the derivatives of the circuit response are preserved to a given order for the

reduced-order model. Another approach towards approximating time responses uses orthogonal polynomials. The basic idea is to project the time response of the original system onto a smaller dimensional subspace spanned by an orthogonal basis. A natural optimality criterion in this setting is the (possibly weighted) square of the approximation error. An example of such an approach can be found in [8], where Chebyshev polynomials are used to approximate the impulse response.

The algorithm that we present in this paper are closest in spirit to the approach in [8]. Motivated by a number of reasons, we propose the use of Laguerre polynomials for the orthogonal basis. Unlike with Chebyshev polynomials that are defined over the interval  $[-1, 1]$ , Laguerre polynomials are defined over  $[0, \infty]$ , making them a more natural candidate for approximating continuous-time responses. In addition, we will show that a closed-form expression for the optimal coefficients can be derived very simply. These coefficients can be *numerically computed* very efficiently using Krylov methods. And the amount of computation required by our methods is the same as that required by PRIMA. We will also show that our algorithm can handle more general inputs, and can accommodate differing time-horizons for the approximation.

## 2 Background

### 2.1 Problem Description

Consider a single-input, single-output<sup>1</sup> RLC network, described by the state equations:

$$M \frac{dx}{dt} + Nx(t) = bu(t), \quad y(t) = Cx(t) \quad (1)$$

where  $x(t) \in \mathbf{R}^n$ ,  $u(t)$  and  $y(t)$  are the state, input, and output respectively.  $M$  and  $N$  are square matrices of size  $n$ , and  $b \in \mathbf{R}^n$ . These matrices arise from modified nodal analysis (MNA). Typically  $n$  is very large.

<sup>1</sup>We consider single-input, single-output systems only for simplicity of exposition; the results presented herein can be extended to multi-input multi-output systems without much difficulty.

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of  $Q_k$  are orthogonal. The proof is in [4].

### 3.3 Efficient computation

As the columns of  $P_k$  span a Krylov subspace, its QR factorization can be efficiently computed as follows:

1. Compute an LU factorization  $M + N$ . Use this to compute  $\tilde{B} = (I + A)^{-1}B = (M + N)^{-1}b$ .
2. Compute the QR factorization of  $P_k$  using the Arnoldi method, without explicitly forming  $\tilde{A}$ .

The overall computation required is essentially the same as that required by PRIMA [4].

## 4 Extensions

### 4.1 Accounting for the time horizon

Recall that with the standard Laguerre approximation, the error was weighted by  $e^{-t}$ ; this introduces implicitly a time horizon of interest. This time horizon can be increased or decreased with a simple time-weighting technique, by replacing  $t$  by  $t/T$ . (Loosely speaking, increasing  $T$  “spreads out” the quality of approximation over larger and larger time horizons.) This corresponds to replacing  $A$  by  $AT$  and  $B$  by  $BT$  in the development thus far, that is, with

$$\tilde{B}_T = \left(\frac{1}{T}I + A\right)^{-1} B \text{ and } \tilde{A}_T = \left(\frac{1}{T}I + A\right)^{-1} A,$$

the  $k$ th order Laguerre approximant lies in the span of

$$\left[ \tilde{B}_T \quad \tilde{A}_T \tilde{B}_T \quad \dots \quad \tilde{A}_T^k \tilde{B}_T \right].$$

When  $T$  is very large compared to the magnitude of the eigenvalues of  $A$ , we have

$$\tilde{B}_T \approx A^{-1}B, \quad \tilde{A}_T \approx \left(I - \frac{1}{T}A^{-1}\right),$$

so that the  $k$ th order Laguerre approximant lies in

$$\text{span} \left[ A^{-1}B \quad A^{-2}B \quad \dots \quad A^{-k+1}B \quad A^{-k}B \right],$$

which coincides with the matrix used by PRIMA to perform the model reduction.

In our experience, we have found that a value of  $T$  that is one-fifth of the reciprocal of the smallest real part of the eigenvalues of  $A$  is a good heuristic for trading off the quality of approximation of the time response and the time horizon.

### 4.2 Response to general inputs

Unlike with existing approximation methods such as those based on Chebyshev polynomials, we show how our approach can directly handle inputs other than impulses. Suppose the input  $u$  is a polynomial of order  $k - 1$ . Then, we

may write

$$\int_0^t u(\tau) d\tau = a_0 L_0(t) + \dots + a_k L_k(t),$$

that is, we first compute the Laguerre expansion of the integral of  $u(t)$ . (Note that the expansion is exact.) Then, it is readily verified that the optimal  $k$ th order Laguerre approximant of the state  $x$  is given by  $x(t) \approx x_k(t) = \sum_{i=0}^k \hat{c}_i L_i(t)$ , where

$$L \begin{bmatrix} \hat{c}_k \\ \hat{c}_{k-1} \\ \vdots \\ \hat{c}_1 \\ \hat{c}_0 \end{bmatrix} = \begin{bmatrix} a_k B \\ a_{k-1} B \\ \vdots \\ a_0 B \end{bmatrix}, \quad (12)$$

with  $L$  being defined in (8). In other words, the optimal Laguerre approximant is obtained by simply changing the right-hand side of the linear equation (7). One consequence of this observation is that the reduced-order model derived in §3.2 is independent of the input when it is a polynomial of order smaller than that of the reduced-order model.

## 5 Numerical Results

We present a few representative circuit examples that demonstrate the performance of the Laguerre model reduction algorithm proposed in this paper. We consider three circuit examples that are either adapted or taken directly from [4, 8].

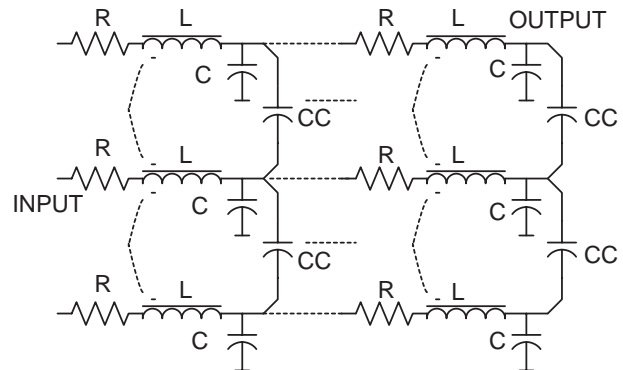


Figure 1. Circuit Example 1 [8].

**Example 1:** The circuit, which is shown in Figure 1, consists of three RLC lines with capacitive and inductive coupling between adjacent lines [8]. There are 20 RLC sections in each line. The circuit parameters for each section are:  $R = 1\Omega$ ,  $L = 1\text{nH}$ ,  $C = 1\text{pF}$ , the coupling capacitance  $CC = 1\text{pF}$ , and the inductance coupling coefficient between adjacent sections is  $k = 0.2$ . The middle line is the aggressor net with a step input. Figure 2 plots the exact response of the far-end node of the upper victim line. In the same plot, we also show the approximation responses of the Laguerre and PRIMA reduced order models, both of which are

of order 13. For a more direct comparison between the Laguerre model reduction method and the PRIMA method, we also plot the absolute differences (errors) between the exact response and the responses of the two reduced order models in Figure 3. The peak errors of the Laguerre model and the PRIMA model, denoted respectively by  $E_{pL}$  and  $E_{pP}$ , are 0.1104V and 0.1113V. The integrals of the errors for the Laguerre model and PRIMA model over the span of the time responses in Figure 2, denoted respectively by  $E_{iL}$  and  $E_{iP}$ , are  $1.36e^{-10}$ V-s (Volt-second) and  $1.71e^{-10}$ V-s. It is evident that the response of the the Laguerre model tracks the exact response more closely as compared to PRIMA.

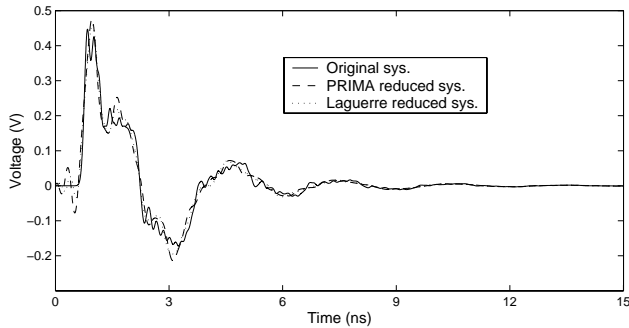


Figure 2. Time responses of Example 1 and its reduced models.

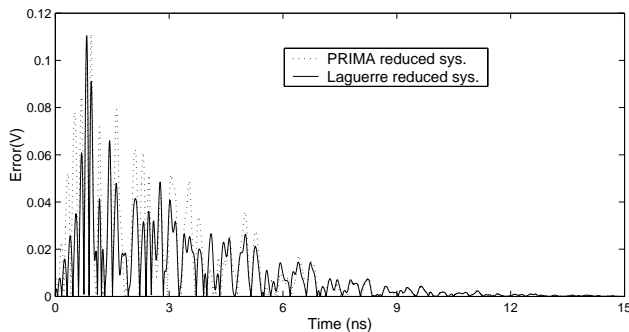


Figure 3. Approximation errors of the reduced models for Example 1.

We perform similar comparisons of the Laguerre model reduction method and the PRIMA method for the remaining two examples. For each circuit, we compare the exact response with the responses of the reduced order models generated by the Laguerre model reduction method and the PRIMA method. The peak errors and the integral errors incurred by the two methods are summarized in Table 1. The Laguerre model reduction method has smaller peak and integral errors as compared to PRIMA. The details of the remaining two examples are as follows:

**Example 2:** The circuit, which is adapted from [8], is shown in Figure 4. Each line is segmented into 40 sections with parameters  $R = 1\Omega$ ,  $L = 0.01nH$ ,  $C = 10fF$ ,  $G = 0.001\Omega$ , and the coupling capacitance  $CC = 1fF$ . The order of the two

Table 1. Peak errors and integral errors of Laguerre and PRIMA models.

Example	1	2	3
$E_{iL} \times 10^{10}$ (V-s)	1.36	0.0316	0.132
$E_{iP} \times 10^{10}$ (V-s)	1.71	0.0404	0.159
$E_{iL}/E_{iP}$	0.792	0.782	0.832
$E_{pL}$ (V)	0.1104	0.508	0.0160
$E_{pP}$ (V)	0.1113	0.700	0.0171
$E_{pL}/E_{pP}$	0.992	0.726	0.936

reduced models is 15. The input is a current source. The responses of the output node of the upper line are shown in Figure 5, and the approximation errors of the Laguerre and PRIMA models are shown in Figure 6.

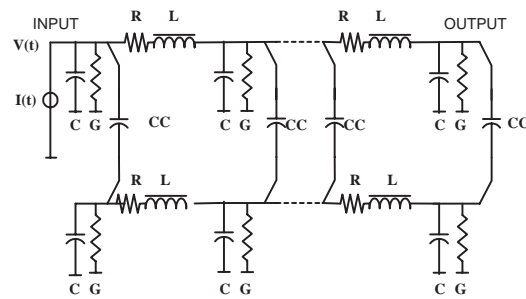


Figure 4. Circuit Example 2 [8].

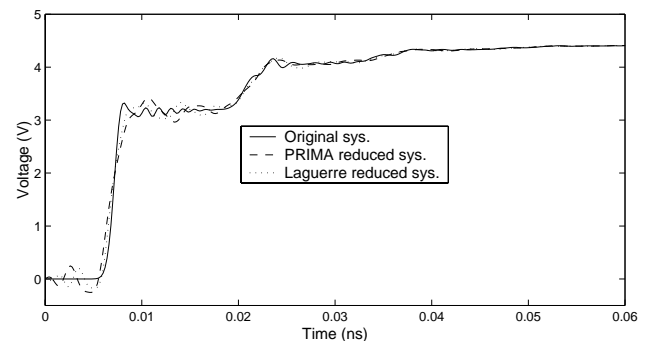
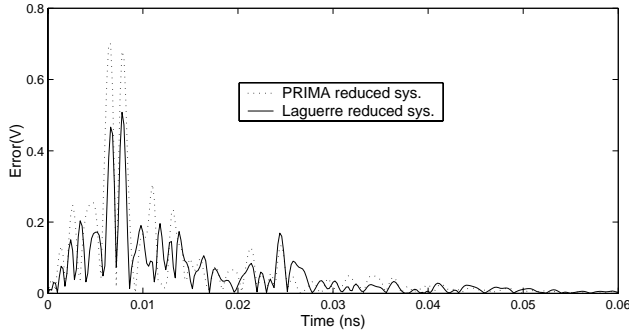
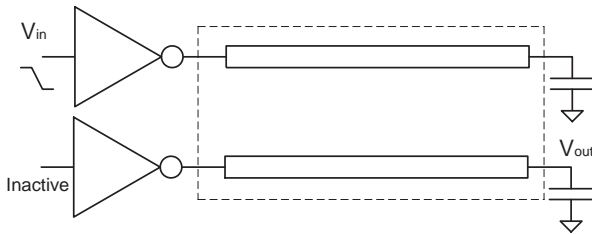


Figure 5. Time responses of Example 2 and its reduced models.

**Example 3:** This example is borrowed from [4]. The circuit is a 2-bit bus driven by CMOS inverters as shown in Figure 7. One of the drivers is switching while the other is quiet. The two wires, consisting of 40 coupled RLC sections each, are modeled as a four-ports system. Each wire has a resistance, inductance, and capacitance (to ground) of  $10\Omega$ ,  $5nH$ , and  $15pF$ , respectively. The total coupling ca-



**Figure 6. Approximation errors of the reduced models for Example 2.**



**Figure 7. Circuit Example 3 [4].**

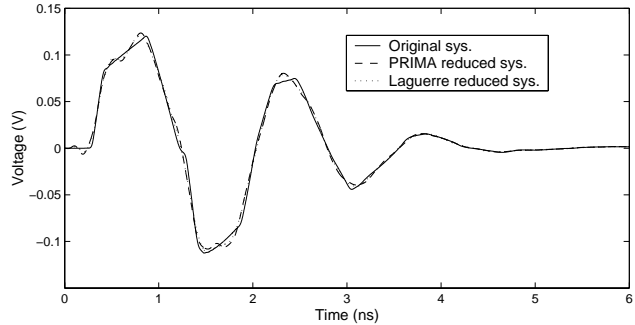
capitance between the two lines is 7.5pF. The order of the reduced models is 10. The input to the upper wire is a ramp input with a rise time of 1ns. The waveforms of the output of the lower wire are shown in Figure 8 and the approximation errors are shown in Figure 9.

## 6 Conclusion

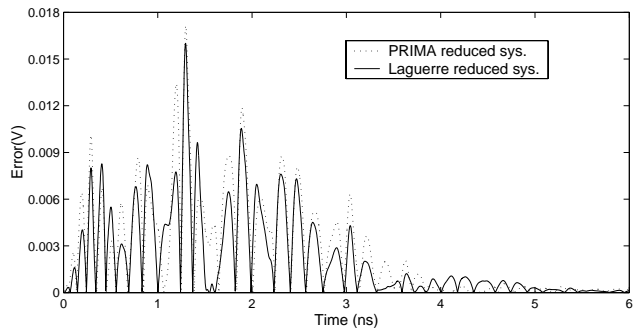
We have presented a new passive model reduction algorithm for large-scale systems based on the Laguerre expansion of the time response. Three distinguishing features of our method are: (i) We use Laguerre expansion to approximate the continuous-time response of the original state equation over  $[0, \infty)$ . (ii) The Laguerre approximation minimizes a weighted square of the approximation error. (iii) We derive a closed-form expression for the optimal Laguerre coefficient matrices, whose orthonormal bases can be numerically computed very efficiently via Krylov methods. Moreover, we introduce a time-weighting technique to further reduce the error over a time horizon of interest. We also explore the connection between the proposed Laguerre approximation and the PRIMA method. Numerical simulations show that our approach can obtain better results than the PRIMA method.

## References

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**Figure 8. Time responses of Example 3 and its reduced models.**



**Figure 9. Approximation errors of the reduced models for Example 3.**

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