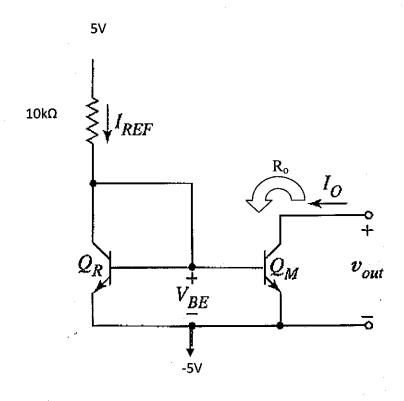
ECE-255 Exam III APRIL/7/2010

Name:			 	
	(Please	print clearly)		
	Student ID:	:		

INSTRUCTIONS

- This is a closed book, closed notes exam.
- Carefully mark your multiple choice answers on the scantron form. Work on multiple choice problems and marked answers in the test booklet will not be graded. Nothing is to be on the seat beside you.
- When the exam ends, all writing is to stop. This is not negotiable. No writing while turning in the exam/scantron or risk an F in the exam.
- All students are expected to abide by the customary ethical standards of the university, i.e., your
 answers must reflect only your own knowledge and reasoning ability. As a reminder, at the very
 minimum, cheating will result in a zero on the exam and possibly an F in the course.
- Communicating with any of your classmates, in any language, by any means, for any reason, at
 any time between the official start of the exam and the official end of the exam is grounds for
 immediate ejection from the exam site and loss of all credit for this exercise.

NOTE: Choose the answer that is closest to what you have calculated; many answers are rounded to the first non-decimal point 1) What is the value of output impedance (R_o) for the current mirror shown below? Assume $\beta=\infty$, $V_{BE}(ON)=0.7V$, $V_A=100V$



(1)∞

(2) $232k\Omega$

(3) $107k\Omega$

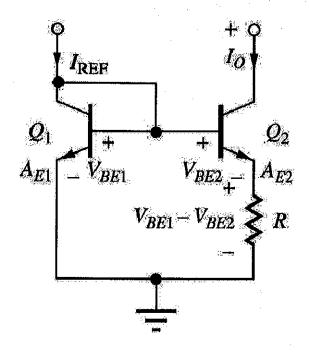
(4)0

(5) $116k\Omega$

$$T_0 = J_{ref} = \frac{10 - 0.7}{10} = 0.93 \text{ mA}$$

$$r_6 = \frac{V_A}{I_6} = \frac{100}{0.93} = 107 \text{ K}$$

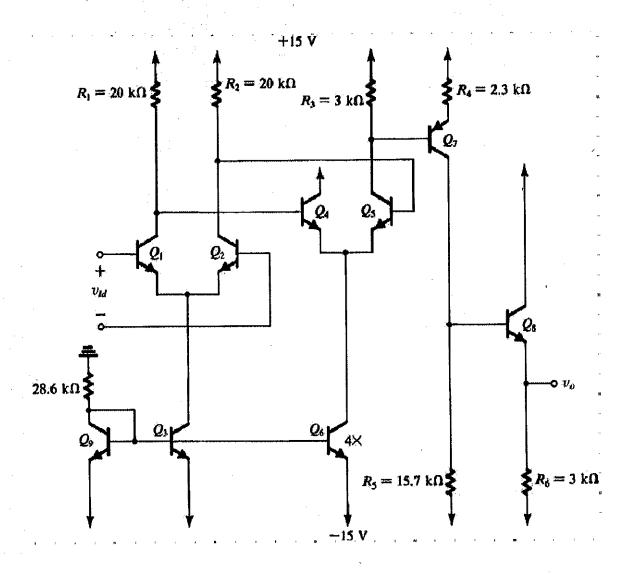
2) For the Widlar current source shown below, what value of R is required to set I_o =25 μ A, if I_{ref} =100 μ A and A_{E2}/A_{E1} =5 Assume V_T =25mV , $V_{BE}(ON)$ =0.7V and ignore base width modulation



- $(1) 3k\Omega$
- $(2) 30k\Omega$
- $(3)300\Omega$
- (4) 150Ω
- (5) $150k\Omega$

$$25 \times 10^{-6} = \frac{0.025}{R} \ln \left(\frac{100}{25} . 5 \right)$$

3) Figure below is internal structure of an OpAmp, what is the current I_{CQ4} ? Assume Q_4 and Q_5 are identical, β =100, and $V_{BE}(ON)$ =0.7V (the circuit is symmetric and since Q_4 and Q_5 are identical I_{CQ4} = I_{CQ5})



(1) 1mA

(2) 2mA

(3) 0.25mA

(4) 5mA

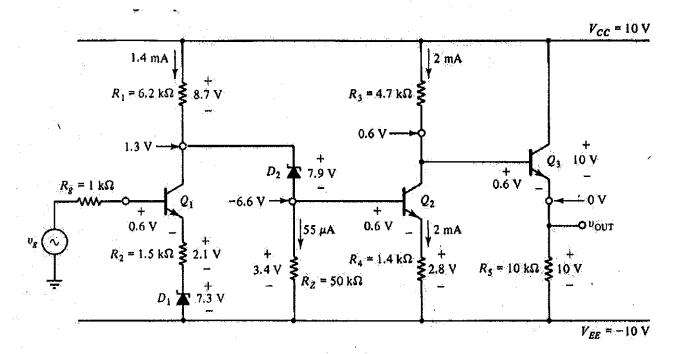
(5) 0mA

$$\frac{T_{ref}}{28.6} = \frac{15 - 0.7}{28.6} = 0.5 \text{ mA}$$

$$\frac{T_{ca6}}{28.6} = 4 \times 0.05 = 2 \text{ mA}$$

$$\frac{T_{ca4}}{28.6} = 1 \text{ mA}$$

4) What is the configuration of the multistage amplifier shown below?

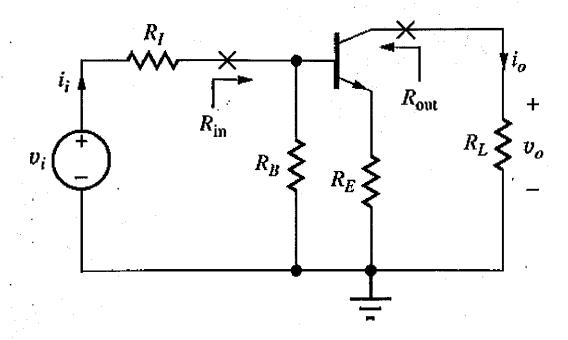


- (1) CC-CE-CE
- (2) CC-CE-CC
- (3) CE-CE-CC
- (4) CE-CE-CE

- (5) CB-CE-CC
- (6) None of the above

CE-CE-CC

5) What are the input impedance of the amplifier shown below, if g_m =20mS, β =75, r_o =100k Ω , R_I =500 Ω , R_B = 15k Ω , R_E =300 Ω , and R_L =12k Ω . V_T =25mV



- (1) $R_{in}=15k\Omega$
- (2) $R_{in}=3k\Omega$
- (3) $R_{in} = 290\Omega$
- (4) R_{in}=100kΩ
- (5) R_{in} =10 $k\Omega$

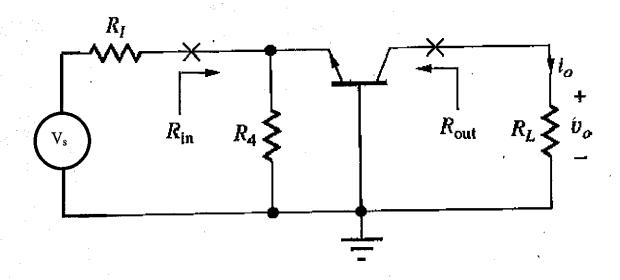
$$r_{\Pi} = \frac{B}{g_{m}} = \frac{75}{0.02} = 3750 \Omega$$

$$R_{in} = R_B \prod \left[r_{ii} + RR_{ii} \right]$$

$$= 15 \prod \left[3.75 + 75 \times 300 \right]$$

$$= 26.25 \times R$$

6) What is the value of the input impedance for the amplifier shown below, if I_C =12.5μA, β=100, V_A =60V, R_I =50Ω, R_4 =100kΩ, and R_L =100kΩ. V_T =25mV



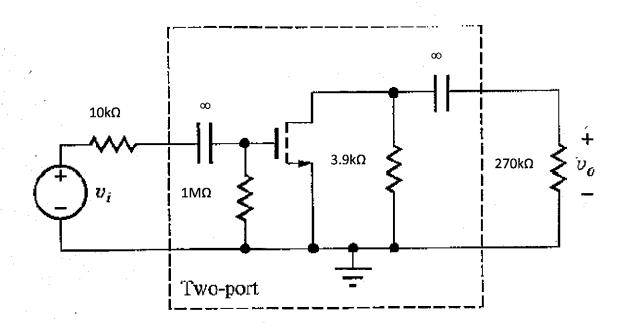
- (1) $R_{in}=100k\Omega$
- (2) $R_{in}=2k\Omega$
- (3) $R_{in}=200k\Omega$
- (4) $R_{in}=50k\Omega$
- (5) R_{in} =500 Ω

$$R_{in} = \frac{I_{c}}{V_{T}} = \frac{12.5 \times 10^{-6}}{0.025} = 5 \times 10^{-44} \frac{A}{V}$$

$$\frac{1}{g_{m}} = r_{e} = 2 \times \Omega$$

$$R_{in} \approx r_{e} = 2 \times \Omega$$

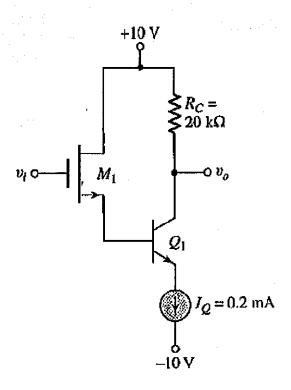
7) What is the absolute value of the voltage gain ($|v_0/v_i|$) for the amplifier whose ac equivalent circuit is shown below? Assume the MOSFET Q points are (2mA, 7.5V), $K_n=1$ mA/V², and $\lambda=0$



- (1) 16
- (2) 8

- (3)540
- (4)25
- (5) gain is zero since we ignored the channel length modulation
- (6) None of the above

8) For the two stage amplifier shown below, what is the absolute value of the voltage gain $A_v=|v_o/v_i|$? Assume for the MOSFET $V_{TN}=1V$, $K_N=0.2mA/V^2$, $\lambda=0$ and for the BJT, $\beta=80$, and $V_A=100V$. $V_T=25mV$



(1)4000

(2) 1

(3)200

(4) 154

(5)80

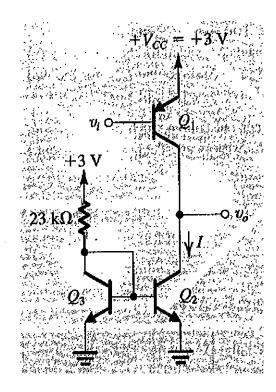
(6) None of the above

$$g_{m1} = \frac{I_{C}}{V_{T}} = \frac{0.2}{25} = 8 \times 10^{-3} \, \text{A} \qquad |V_{0}| = \frac{V_{A}}{I_{C}} = .588^{\text{KA}}$$

$$8/T \quad A_{V} = 8 \times 10^{-3} \times \left[560 \, 1/20^{\text{K}} \right] = 154$$

First stage gain NI

9) What is the absolute value of the voltage gain ($|v_o/v_i|$) of the following amplifier biased with a current source, Assume $\beta=100$, and $V_A=80V$ for all transistors. $V_{BE}(ON)=0.7V$

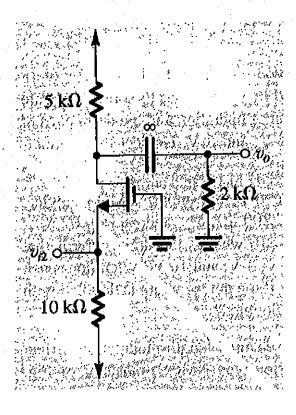


- (1) 1600
- (2) 160
- (3)1
- (4)3200
- (5)800

$$T = Inef = \frac{3 - 0.7}{23} = 0.1 \text{ mA}$$

$$g_{1} = \frac{0.1}{25} = 4 \times 10^{-3} \frac{A}{V}$$

10) What is the absolute value of the voltage gain of the amplifier shown below ($|v_o/v_{i2}|$), Assume $g_m=5mA/V$ and r_o is large.



- (1) 25
- (2) 10
- (3) 1
- (4) 14
- (5)7