

ECE-255 Final Exam

December 13, 2013

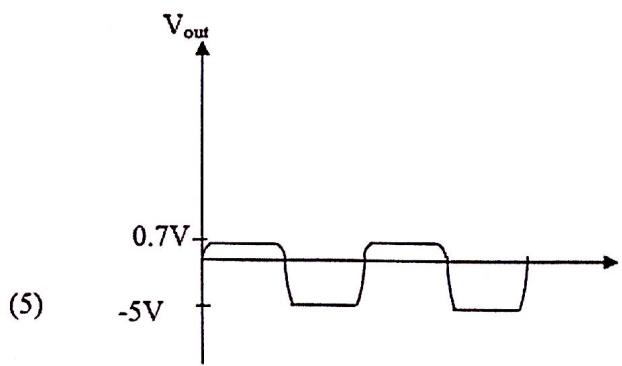
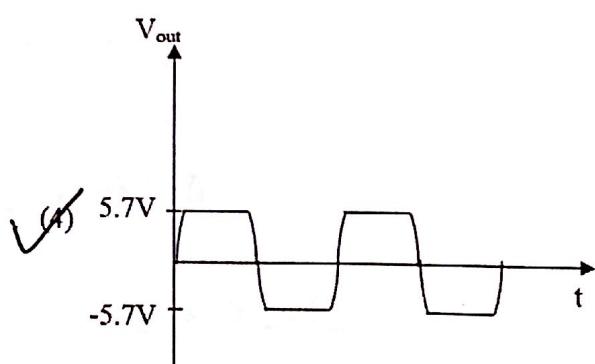
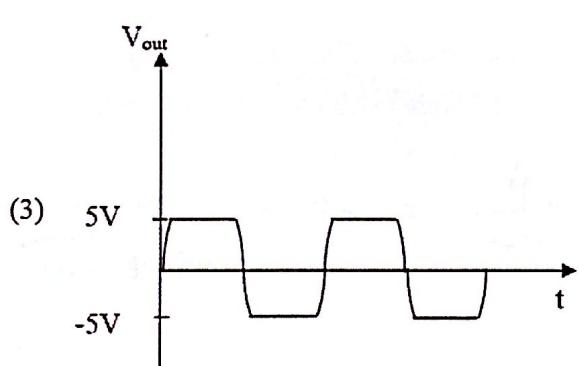
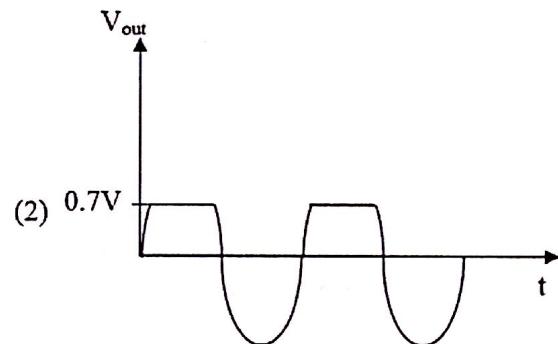
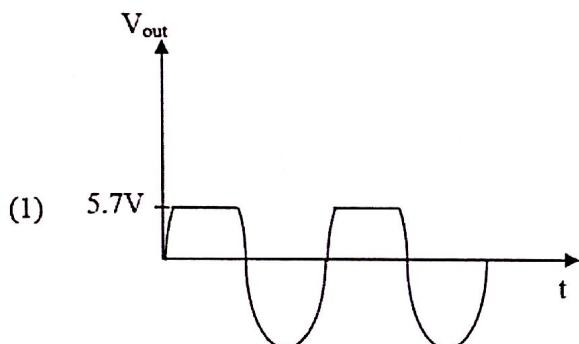
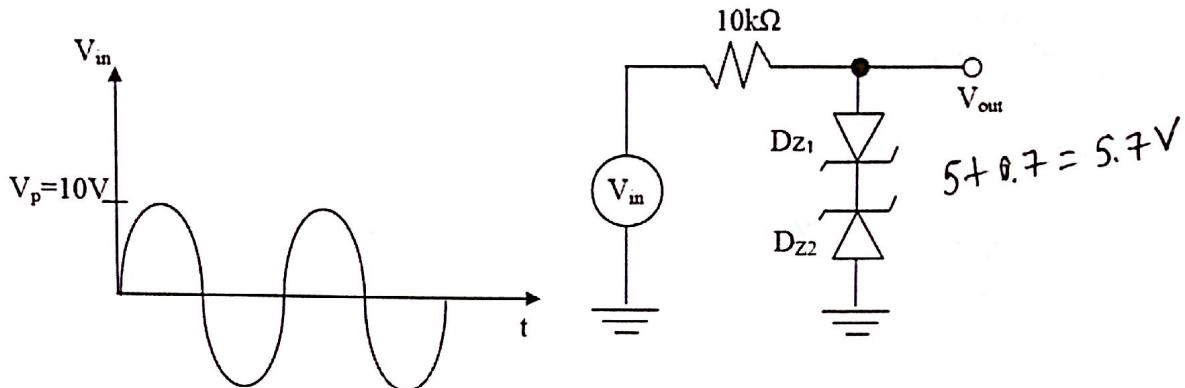
Name: Chetas Joshi
(Please print clearly)

Student ID: Solution

INSTRUCTIONS

- This is a closed book, closed notes exam.
- Clearly mark your multiple choice answers in the scantron.
- When the exam ends, all writing is to stop. This is not negotiable. No writing while turning in the exam/scantron or risk an F in the exam.
- All students are expected to abide by the customary ethical standards of the university, i.e., your answers must reflect only your own knowledge and reasoning ability. As a reminder, at the very minimum, cheating will result in a zero on the exam and possibly an F in the course.
- Communicating with any of your classmates, in any language, by any means, for any reason, at any time between the official start of the exam and the official end of the exam is grounds for immediate ejection from the exam site and loss of all credit for this exercise.

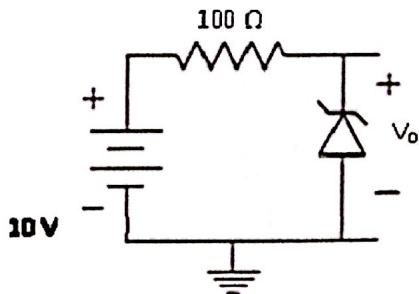
- 1) For the circuit shown below, if a sinusoidal wave with a peak voltage of 10 V is applied to the input, which one of the curves represent the output voltage? ($V_{on} = 0.7V$, $V_Z = 5V$)



(6) None of these

2) The Zener diode in the circuit has an equivalent resistance $R_Z = 20 \Omega$. From the diode data sheet we find that if the voltage across the Zener diode is 6.4 V at $I_Z = 20 \text{ mA}$. Determine the output voltage V_{out} .

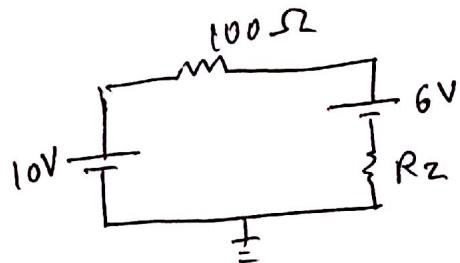
- (1) $V_{\text{out}} = 6.4 \text{ V}$ (2) $V_{\text{out}} = 6.0 \text{ V}$
 ✓ (3) $V_{\text{out}} = 6.67 \text{ V}$ (4) $V_{\text{out}} = 5.33 \text{ V}$
 (5) $V_{\text{out}} = 10 \text{ V}$ (6) None of the above



$$V_Z = V_{Z0} + R_Z I_Z$$

$$6.4 = V_{Z0} + 20(20) \text{ mV}$$

$$V_{Z0} = 6.4 - 0.4 = 6 \text{ V}$$

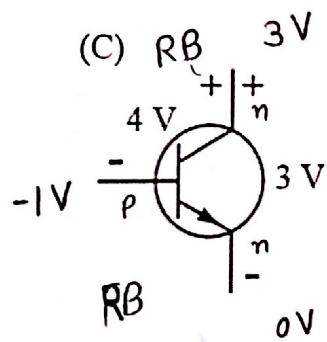
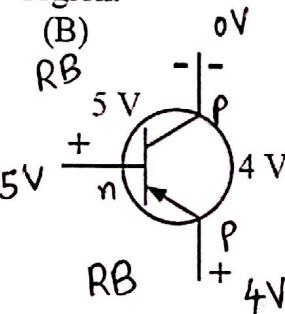
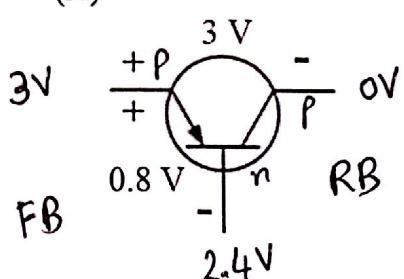


$$\frac{10 - 6}{100 + 20} = I_Z = \frac{4}{120} = 0.033 \text{ A}$$

$$\begin{aligned} V_o &= 6 + I_Z R_Z \\ &= 6 + (0.033)(20) \\ &= 6.67 \text{ V} \end{aligned}$$

3) For the three bipolar transistors shown below, identify which one or two devices are operated at "Forward Active" region.

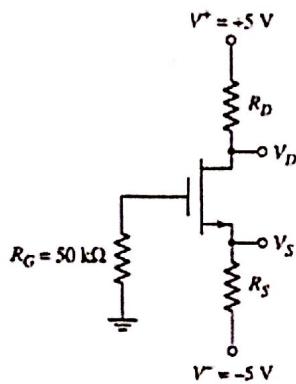
(A)



Which device or devices are operated at "Forward active regions"?

- (1) A
- (2) C
- (3) B
- (4) A and B
- (5) B and C
- (6) None of the above.

4) MOSFET circuit:

Circuit Values:

$I_D = 2.5 \text{ mA}$

$R_D = 1 \text{ k}\Omega$

$R_S = 1 \text{ k}\Omega$

MOSFET Values:

$V_{TN} = 1.5 \text{ V}$

$k_n' = 1 \text{ mA/V}^2$

$\lambda = 0 \text{ V}^{-1}$

(a) Determine the MOSFET's DC drain-to-source voltage, V_{DS} . (b) What is the (W/L) ratio required for the MOSFET to satisfy the above biasing conditions?

- (1) -4V; 10.0 (2) 5V; 2.5 (3) 3V; 2.5
 (4) 4V; 2.0 (5) 5V; 5.0 (6) None of the above

$$V_G \approx 0 \text{ V}, \quad V_D = 5 - I_D R_D = 5 - (2.5)(1) = 2.5 \text{ V}$$

$$V_S = -5 + I_D R_S = -5 + (2.5)(1) = -2.5 \text{ V}$$

$$\underline{V_{DS} = 5 \text{ V}}$$

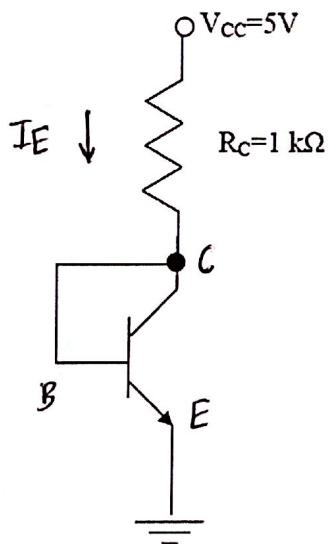
$$V_{GS} = 2.5 \text{ V} > V_{TN} = 1.5 \text{ V} \quad V_{DS} > V_{GS} - V_{TN} \quad (\text{sat}^n)$$

$$I_D = \frac{1}{2} k_n' \frac{W}{L} (V_{GS} - V_{TN})^2$$

$$2.5(2) = \frac{W}{L} (1)^2 \Rightarrow \underline{\frac{W}{L} = 5}$$

5) For the bipolar circuit shown below, I_C ?

$$\beta = 80, V_{BE(on)} = 0.7, V_T = 25 \text{ mV}$$



- (1) 4.3mA (2) 28.7μA (3) 2.87mA ✓(4) 4.25mA
 (5) 0.425mA (6) None of the above

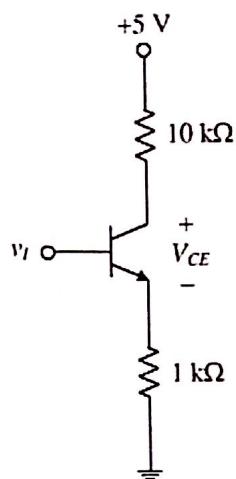
$$V_C = V_B = 0.7 \text{ V}$$

$V_C = V_B$ (ensures FA region)

$$I_E = \frac{5 - 0.7}{1} = 4.3 \text{ mA}$$

$$I_C = \alpha I_E = \frac{\beta}{\beta + 1} I_E = \frac{80}{81} (4.3) = 4.25 \text{ mA}$$

6) Find the value of V_{CE} in the BJT circuit shown below for $v_I = 0V$.



BJT Values:

$$\beta = 200$$

$$V_{BEON} = 0.7 \text{ V}$$

$$V_{CE-SAT} = 0.1 \text{ V}$$

$$V_A = \infty \text{ V}$$

$$V_T = 26 \text{ mV}$$

- (1) 0V
(4) 1.3V

- (2) 0.1V
✓ (5) 5V

- (3) 0.7V
(6) None of the above

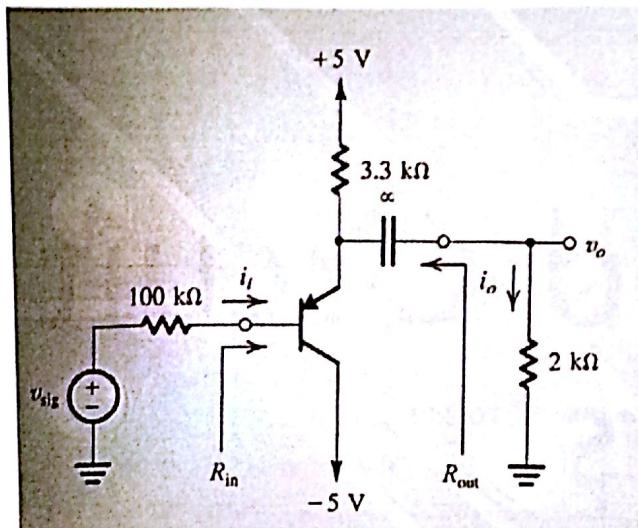
$$V_T = 0V$$

If transistor is ON, $V_{BE} = 0.7V \Rightarrow V_E = -0.7V$

If transistor is ON, $V_{BE} = 0$, $I_E = I_C + I_B$
But then direction of current is into the Emitter which
is not possible. so transistor is off. No current.

$$V_F = 0V, \quad V_C = 5V \Rightarrow V_{CE} = 5V$$

7) The value of g_m in the BJT transistor shown below is? Assume $\beta=50$, $V_{BE(on)}=0.7$, $V_T=25$ mV.



(1) 16 mA/V
 (4) 25 mA/V

(2) 8 mA/V
 (5) 64 mA/V

(3) 32 mA/V
 ✓ (6) None of the above

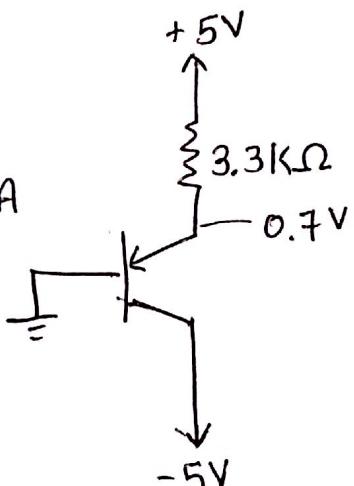
DC analysis,

$$I_E = \frac{5 - 0.7}{3.3} = \frac{4.3}{3.3} = 1.3 \text{ mA}$$

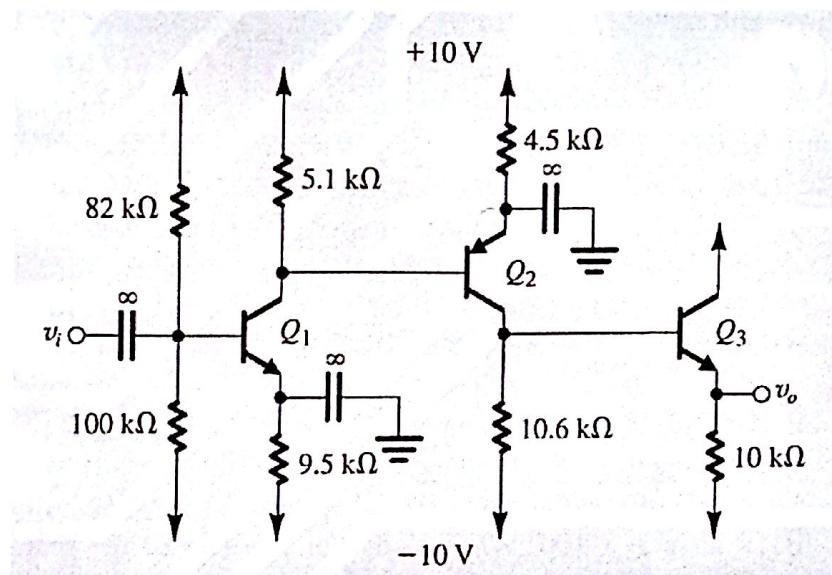
$$I_C = \alpha I_E = \frac{50}{51} (1.3) = 1.28 \text{ mA}$$

$$V_{EC} > 0.2 \text{ V} \Rightarrow \text{confirmed FA}$$

$$\begin{aligned} g_m &= I_C / V_T \\ &= 1.28 / 25 = 51.2 \text{ mA/V} \end{aligned}$$

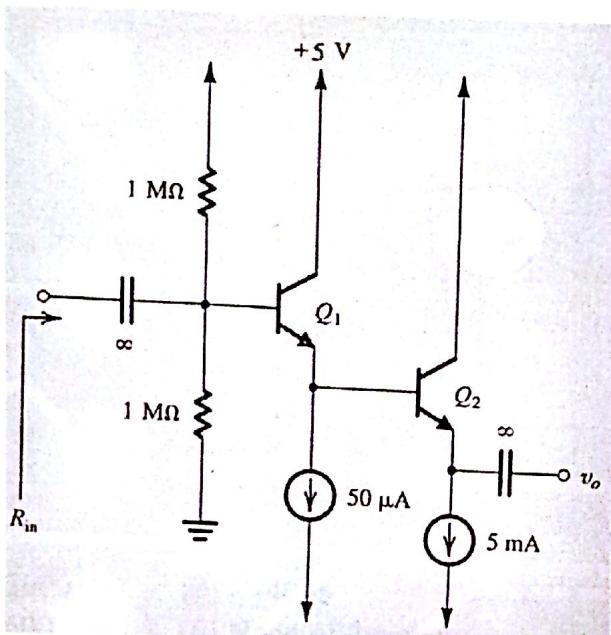


8) The multistage amplifier shown below has which of the following configuration:

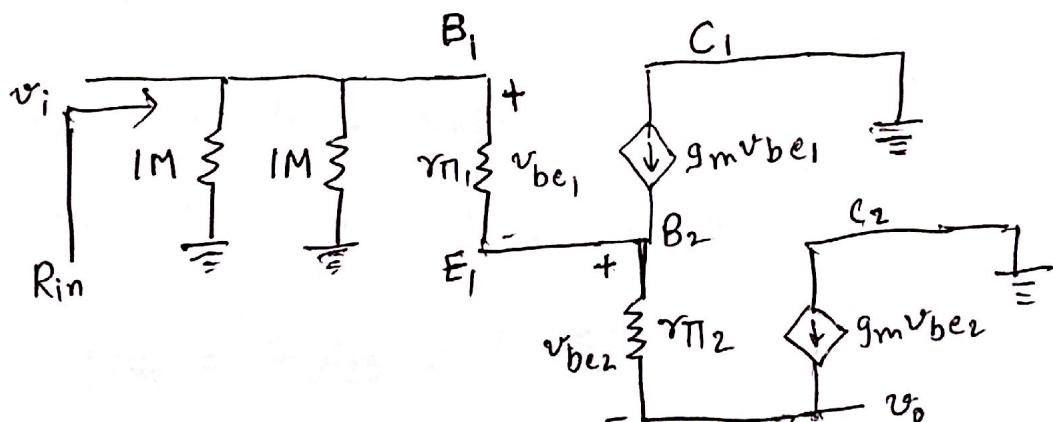


- (1) CC-CE-CE (2) CC-CE-CC
 (4) CE-CE-CE (5) CB-CE-CC (6) CE-CE-CC
 (6) None of the above

9) What is the input impedance of the two stage amplifier shown below? Assume $\beta=100$, $V_{BE(on)}=0.7$ mV.

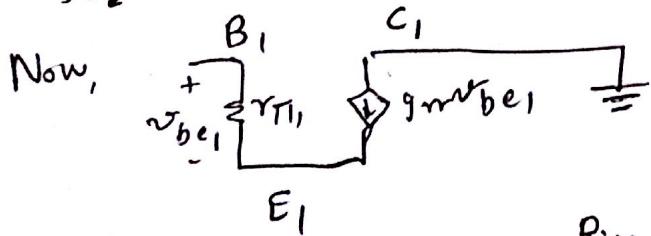


- (1) $\approx 500\text{k}\Omega$ (2) $\approx 75\text{k}\Omega$ (3) $\approx 250\text{k}\Omega$ (4) $\approx 65\text{k}\Omega$
 (5) $\approx 25\text{k}\Omega$ (6) None of the above



$$v_{be2} = -g_m v_{be2} \gamma \pi_2 \quad E_2$$

$$\Rightarrow v_{be2} = 0 \rightarrow \text{No current through } \gamma \pi_2$$

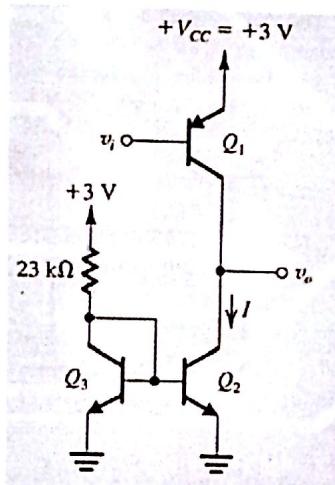


$$v_{be1} = 0$$

$$\Rightarrow \text{No current through } \gamma \pi_1$$

$$R_{in} = 1\text{M} \parallel 1\text{M} = 500\text{k}\Omega$$

- 10) What is the gain (v_o/v_i) of the CE amplifier shown below, assume $\beta=100$, $V_{BE(on)}=0.7$, $V_T=25$ mV, and $V_A=100V$ for all transistors (Hint: R_L is the total load resistance seen at the output node)



- (1) ≈ -2000 (2) ≈ -4000 (3) ≈ 1 (4) ≈ -1000
 (5) ≈ -500 (6) None of the above

$Q_2 - Q_3$ (Current Mirror)

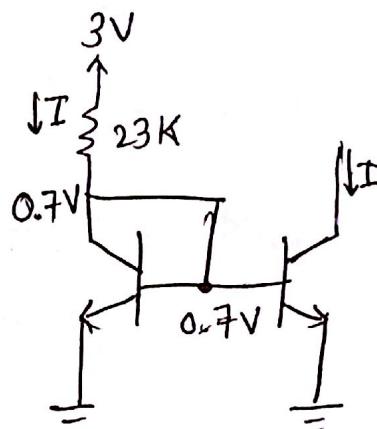
$$I = \frac{3 - 0.7}{23K} = \frac{2.3}{23} = 0.1 \text{ mA}$$

$$Q_1 (\text{CE}) \text{ gain } \frac{v_o}{v_i} = -g_m R_L$$

$$R_L = r_o(Q_2) = \frac{V_A}{I_C} = \frac{100}{0.1} = 1 \text{ M}\Omega$$

$$g_m = I_C / V_T = 0.1 / 25 = 4 \text{ mA/V}$$

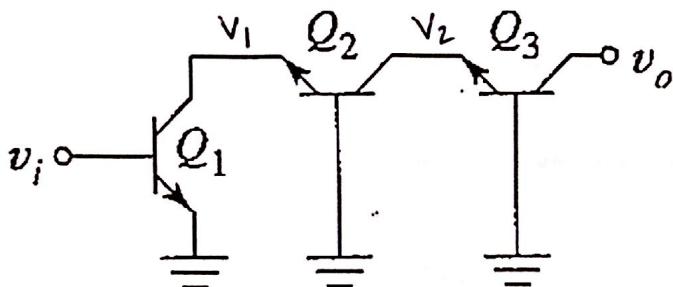
$$\frac{v_o}{v_i} = -4(1) = -4000$$



11) For the amplifier shown below known as double cascode, what is the gain $|Av|$? Q_2 and Q_3 both have twice the Early voltage as that of Q_1 . But they all have the same β .

Note: $I_{c1}=I_{c2}=I_{c3}$, $g_m=g_{m1}=g_{m2}=g_{m3}$, $2r_{o1}=r_{o2}=r_{o3}$, $r_{\pi1}=r_{\pi2}=r_{\pi3}$

Assume $r_o \gg 1/g_m$ for all three



- (1) $g_m r_{o1}$ (2) $g_m^3 r_{o1}^3$ ✓ (3) $2g_m r_{o1}$ (4) $g_m^2 r_{o1}^2$
 (5) $4g_m r_{o1}$ (6) None of the above

$$Q_1 (CE) : \frac{v_1}{v_i} = -g_m R_L \\ = -g_m (r_{o1} \parallel r_e) = -g_m (r_{o1} \parallel \frac{1}{g_m})$$

$$r_e = \alpha/g_m \approx 1/g_m$$

since, $r_o \gg 1/g_m$

$$\frac{v_1}{v_i} = -g_m (1/g_m) = -1$$

$$Q_2 (CB) : \frac{v_2}{v_1} = g_m R_L \quad \text{here } R_L = r_e (Q_3) \\ = g_m (1/g_m) = 1 = 1/g_m$$

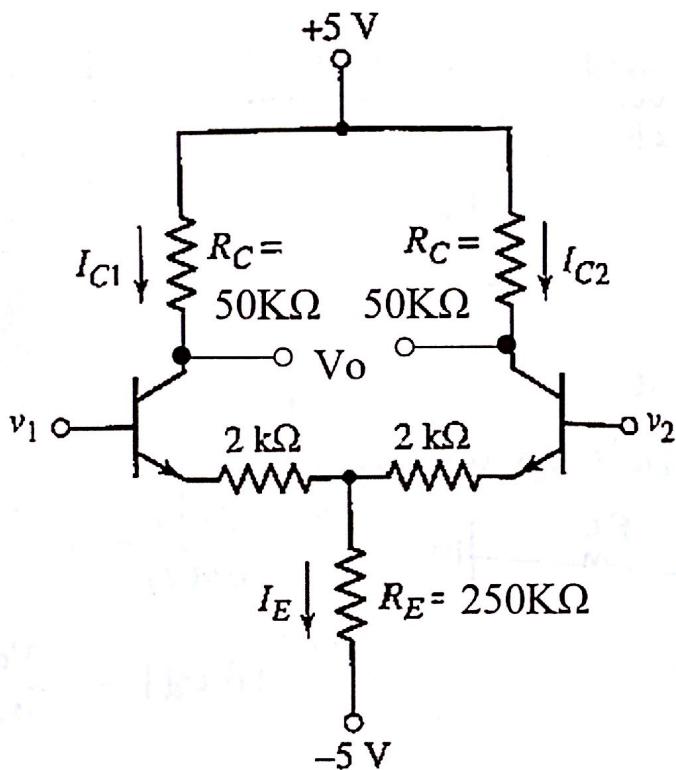
$$Q_3 (CB) : \frac{v_o}{v_2} = g_m R_L = g_m r_{o3}$$

$$\Rightarrow \frac{v_o}{v_i} = \frac{v_1}{v_i} \times \frac{v_2}{v_1} \times \frac{v_o}{v_2} = -1(1)(g_m r_{o3}) = -2g_m r_{o1}$$

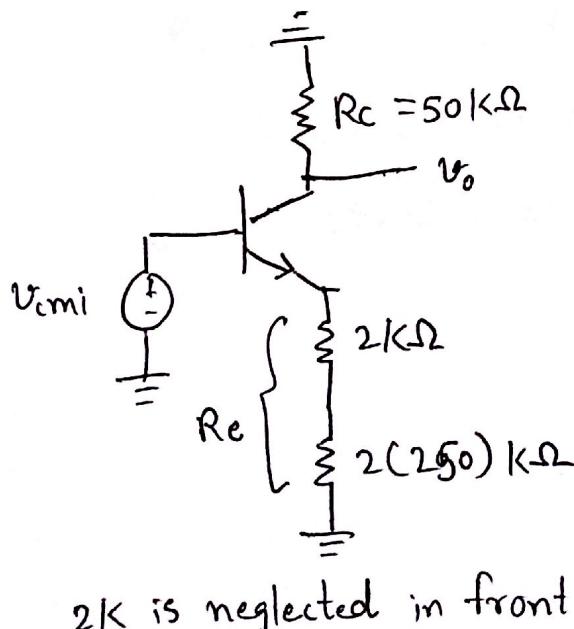
$$|Av| = 2g_m r_{o1}$$

12) What is the common-mode gain ($|Av_c|$) for the circuit shown below? Assuming $\beta_0 \gg 1$ and $r_o \gg 1$. (hint: use the half circuit model; draw small signal model of BJT to calculate v_{oc}/v_{ic} at $v_{id}=0$; 2 k Ω resistor can be regarded as part of transistor or it's much smaller than 50 k Ω .)

What is the differential mode gain ($|Av_d|$) for the circuit shown below?
[Answers: first common-mode gain; second differential mode gain]



- (1) ≈ 0.1 ; ≈ 25 (2) ≈ 0.1 ; ≈ 50 (3) ≈ 0.2 ; ≈ 50 (4) ≈ 0.2 ; ≈ 25
 (5) ≈ 25 ; ≈ 0.1 (6) None of the above



Gain of common CE with R_E is

$$\frac{v_o}{v_{cni}} = -\frac{g_m R_C}{1 + g_m R_E}$$

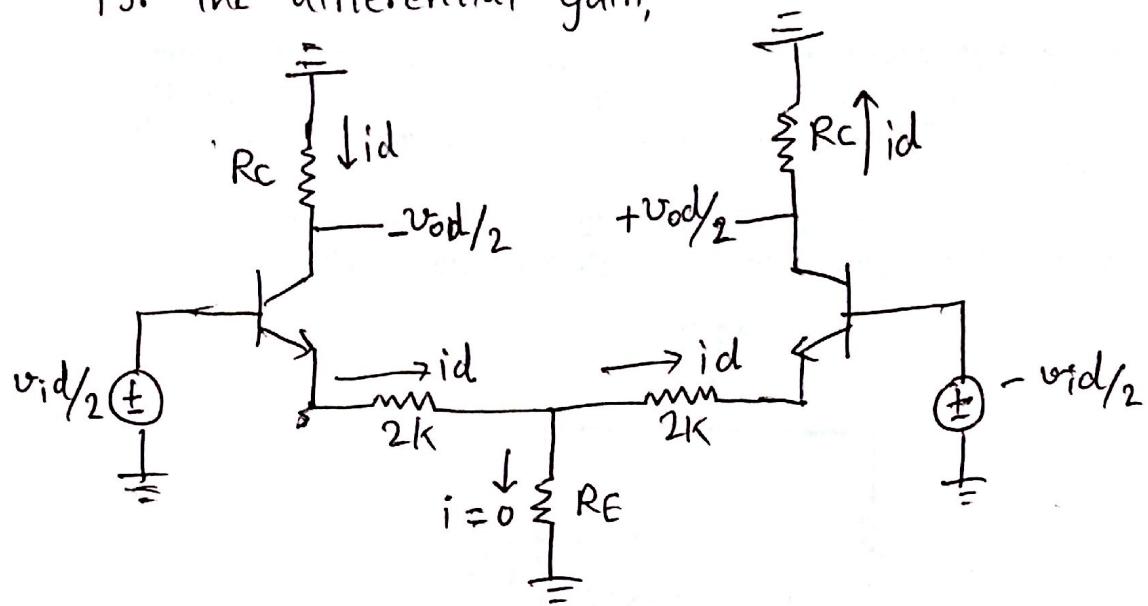
$$g_m = I_C / V_T = \beta I_B / V_T$$

$$g_m R_E \gg 1 \Rightarrow \frac{v_o}{v_{cni}} = -\frac{g_m R_C}{g_m R_E}$$

$$|Av_c| = \frac{R_C}{R_E} = \frac{50 \text{ k}}{2(250) \text{ k}} \approx 0.1$$

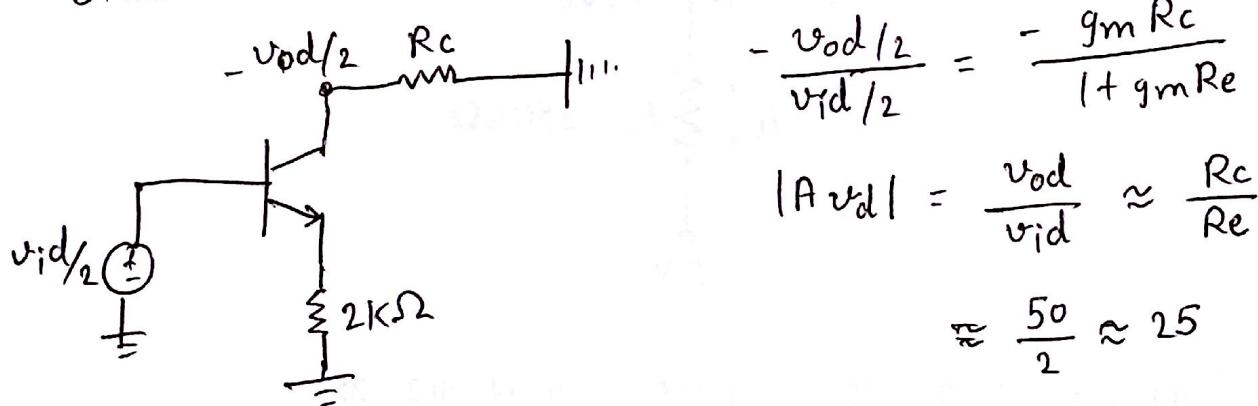
2 k is neglected in front of 500 k.

For the differential gain,



$$A_d = \frac{v_{0d}}{v_{id}}$$

Differential Half circuit



$$-\frac{v_{0d}/2}{v_{id}/2} = -\frac{g_m R_c}{1 + g_m R_e}$$

$$|A v_{0d}| = \frac{v_{0d}}{v_{id}} \approx \frac{R_c}{R_e}$$

$$\approx \frac{50}{2} \approx 25$$

25 dB about 25 enough to need

~~25 dB ref 10 mV~~
Amp + 10 mV

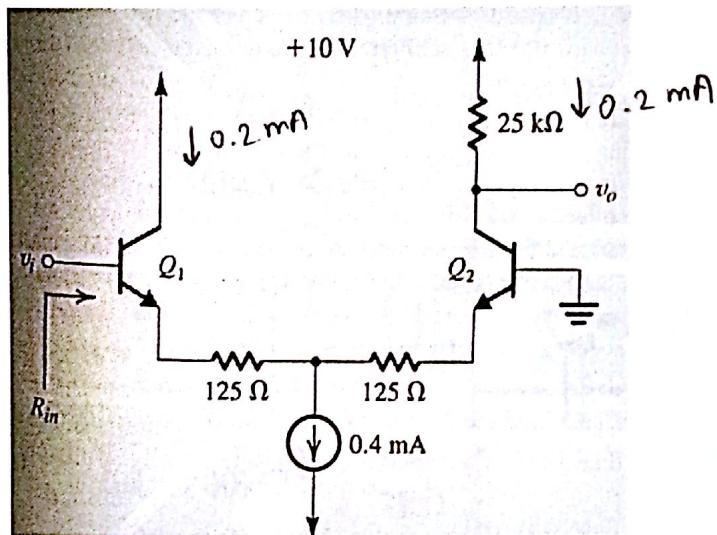
Amp + 10 mV

Start with 10 mV
Get 100 mV

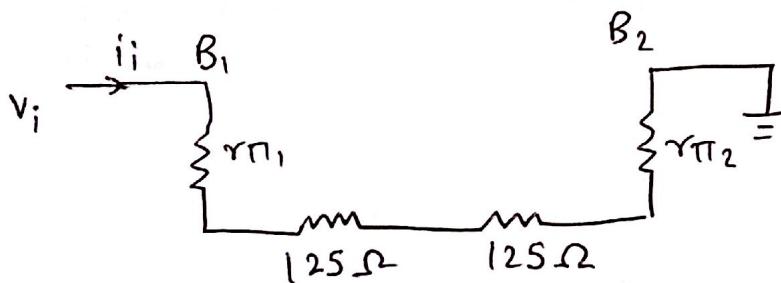
100 mV \rightarrow 1000 mV

1000 mV \rightarrow 10 V

- 13) What is the differential mode input impedance of the amplifier shown below? assume $\beta=100$, $V_{BE(on)}=0.7$, $V_T=25$ mV, and $V_A=100V$.



- (1) $50\text{ k}\Omega$ (2) $250\text{ }\Omega$ (3) $25\text{ k}\Omega$ (4) $25.25\text{ k}\Omega$
 (5) $12.625\text{ k}\Omega$ (6) None of the above



$$\gamma_{\pi_1} = \gamma_{\pi_2} = \beta/g_m$$

$$g_m = I_c/V_T$$

$$\begin{aligned} R_{in} &= \gamma_{\pi_1} + \gamma_{\pi_2} + 250\text{ }\Omega \\ &= 2(12.5)\text{ k}\Omega + 250\text{ }\Omega \\ &= 25.25\text{ k}\Omega \end{aligned}$$

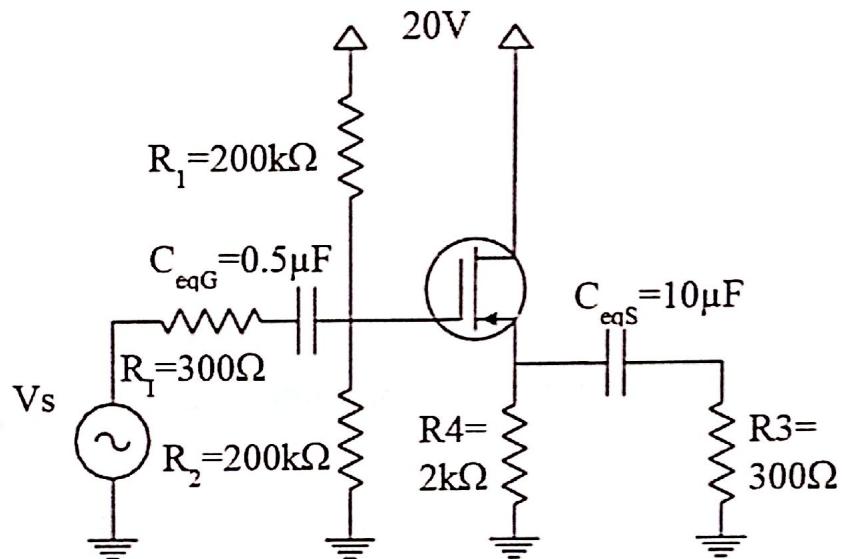
$$= \frac{0.2}{25}$$

$$= 0.008\text{ A/V}$$

$$\gamma_{\pi} = 100 / 0.008$$

$$= 12.5\text{ k}\Omega$$

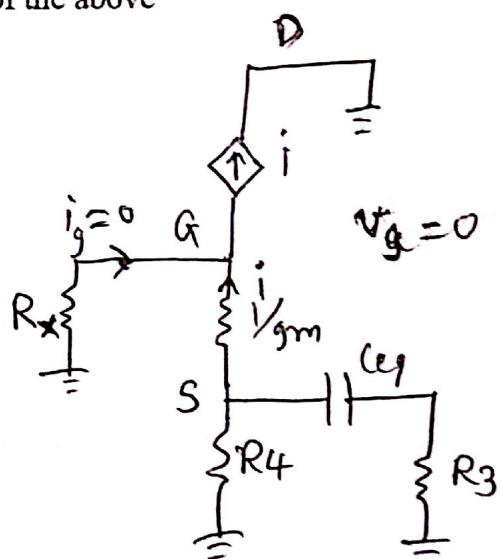
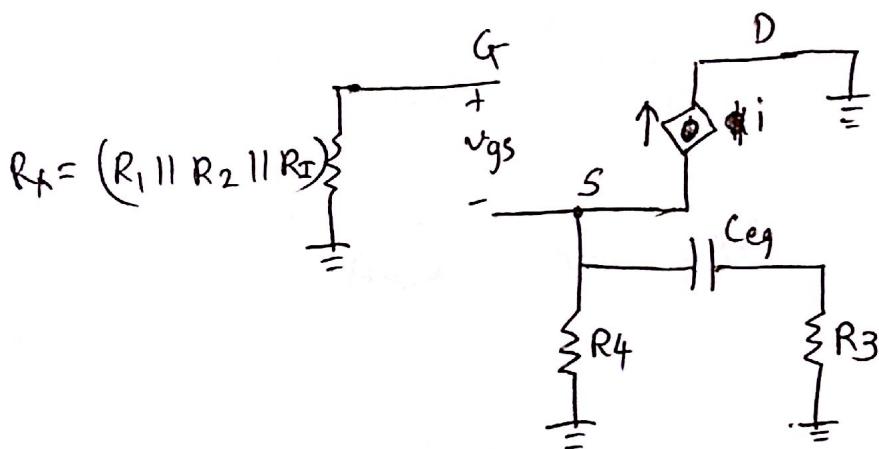
- 14) For the common drain amplifier shown below, the transconductance of the MOSFET is $g_m = 10 \text{ mS}$. Due to the high impedance at the output, the low cut-off frequency, ω_L , is dominated by the output pole: $\omega_L \approx \omega_{OL} \approx \frac{1}{R_{eqS} C_{eqS}}$



R_{eqS} is the equivalent resistance "looking into" the output capacitor, C_{eqS} . What is the value of R_{eqS} ?

- (1) $R_{eqS} = 395.2\Omega$ (2) $R_{eqS} = 300.0\Omega$ (3) $R_{eqS} = 260.9\Omega$
 (4) $R_{eqS} = 75.0\Omega$ (5) $R_{eqS} = 360.9\Omega$ (6) None of the above

(π Model)



$$\begin{aligned}
 R_{eqS} &= (R_4 \parallel \frac{1}{gm}) + R_3 \\
 &= 2k \parallel \frac{1}{10m} + 300 \\
 &= 0.095 \Omega + 300 \Omega = 395.2 \Omega
 \end{aligned}$$

15) Find A_{mid} for this transfer function.

$$A_v(s) = \frac{10^{10} s^2 (s+1)(s+200)}{(s+3)(s+5)(s+7)(s+100)^2 (s+300)}$$

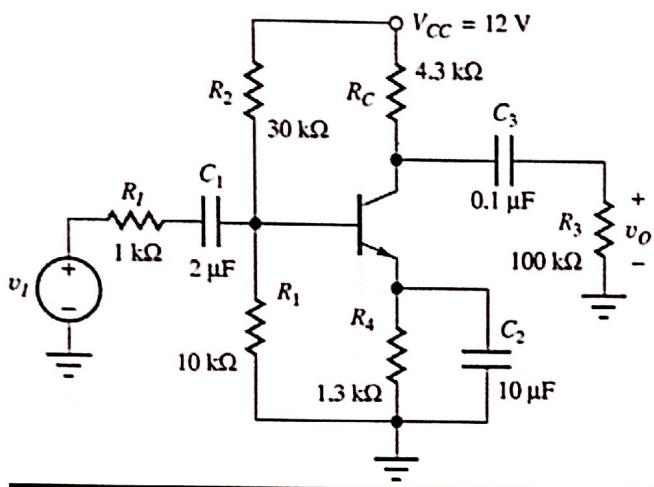
- (1) 1
 ✓ (2) 10^{10}
 (3) 2×10^{12}
 (4) 3.33×10^5
 ✓ (5) 6.67×10^5
 (6) None of the above

~~Compare with low frequency gain expression~~

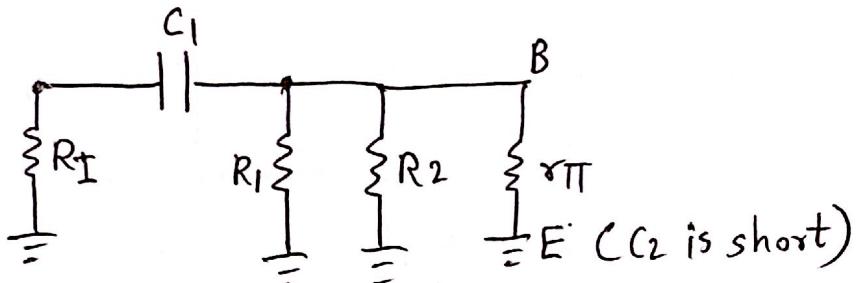
$$\begin{aligned}
 A_v(s) &= 10^{10} \left(\frac{s^2(s+1)}{(s+3)(s+5)(s+7)} \right) \left(\frac{(s+200)}{(s+100)^2(s+300)} \right) \\
 &= 10^{10} \frac{s^2(s+1)}{(s+3)(s+5)(s+7)} \frac{200(1+s/200)}{(100)^2 (1+s/100)^2} \frac{300(1+s/300)}{(1+s/100)^2 (1+s/300)^2} \\
 &= \frac{10^{10}}{10^4} \frac{200}{300} \frac{s^2(s+1)}{(s+3)(s+5)(s+7)} \frac{(1+s/200)}{(1+s/100)^2 (1+s/300)^2} \\
 A_{mid} &= 0.67 \times 10^6
 \end{aligned}$$

↓ ↓ ↓
 low freq. Response high freq. Response

- 16) Short circuit time constant method is an important technique to determine the low cut-off frequency using equation: $\omega_L \approx \sum_{i=1}^3 \frac{1}{R_i S C_i}$. For the circuit below, what is the R_{IS} for the BJT base related loop? Here $r_\pi = 1.51 \text{ k}\Omega$ and $\beta_0 = 99$ and $r_o = 46.8 \text{ k}\Omega$.



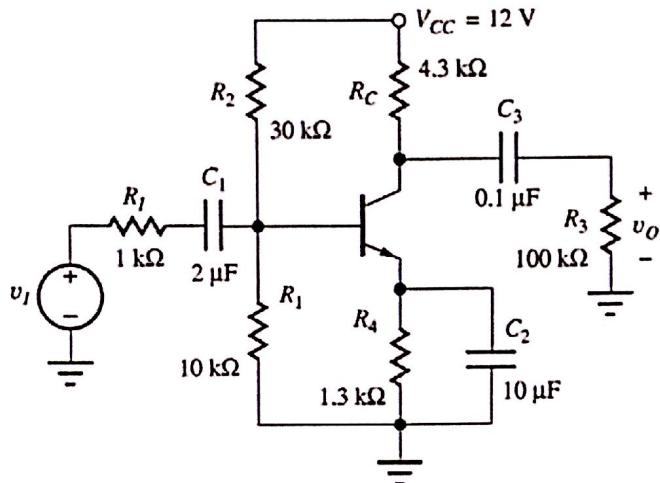
- (1) 4.88 kΩ
- (2) 3.044 kΩ
- (3) 7.095 kΩ
- (4) 8.095 kΩ
- (5) 2.26 kΩ
- (6) None of the above



$$\begin{aligned}
 R_{IS} &= R_I + (R_1 \parallel R_2 \parallel r_\pi) \\
 &= 1\text{k} + (10\text{k} \parallel 30\text{k} \parallel 1.51\text{k}) \\
 &= 1\text{k} + 1.26\text{k} = 2.26\text{k}\Omega
 \end{aligned}$$

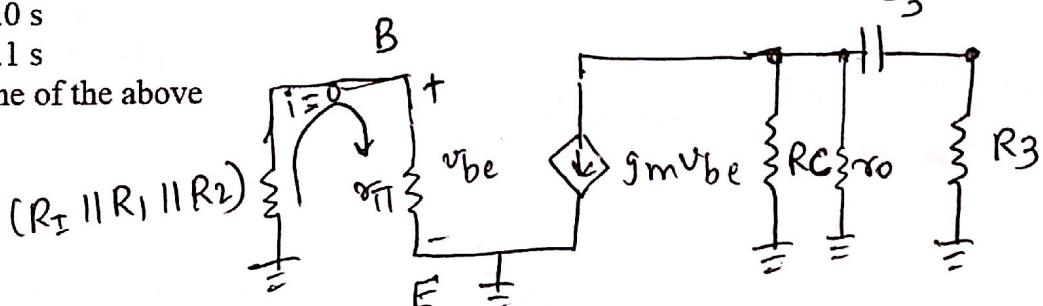
$$R_{IS} C_1 = 2.26 \times 2 \text{ ms} = 4.52 \text{ ms} = 0.0045$$

17) Short circuit time constant method is an important technique to determine the low cut-off frequency using equation: $\omega_L \cong \sum_{i=1}^3 \frac{1}{R_i S C_i}$. For the circuit below (the same as in Problem 16), what is the time constant for the BJT collector related loop? Here $r_\pi = 1.51 \text{ k}\Omega$, $\beta_0 = 99$, and $r_o = 46.8 \text{ k}\Omega$.



$$v_{be} = 0$$

- (1) 96.1 rad/s
- (2) ~ 0.01 s
- (3) 0.96 rad/s
- (4) ~ 1.0 s
- (5) ~ 0.1 s
- (6) None of the above



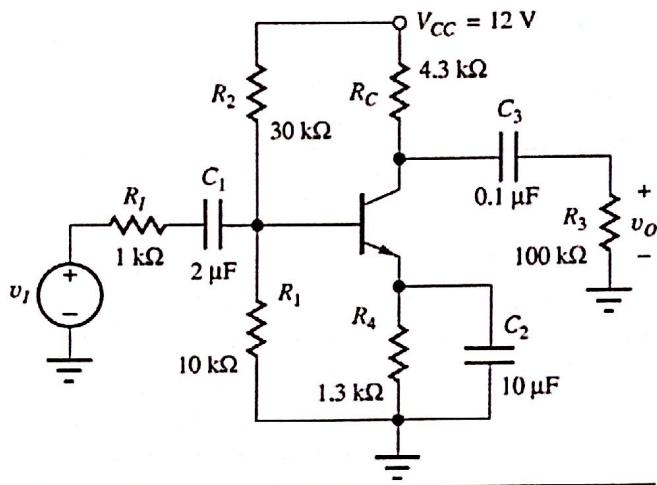
$$\begin{aligned} R_{3S} &= (R_C \parallel r_o) + R_3 \\ &= (4.3 \parallel 46.8) \text{ k} + 100 \text{ k} \end{aligned}$$

$$= 102.2 \text{ k}\Omega$$

$$\text{Time const} = R_{3S} C_3 = 102.2 \times 0.1 \text{ ms} = 10.2 \text{ ms} = 0.01 \text{ s}$$

~~97.85 ms~~

- 18) Short circuit time constant method is an important technique to determine the low cut-off frequency using equation: $\omega_L \approx \sum_{i=1}^3 \frac{1}{R_i C_i}$. For the circuit below (the same as in Problems 16 and 17), what is ω_L for the whole BJT related circuit if R_{2S} in the emitter loop is known as 23.3Ω ? Here $r_\pi = 1.51\text{ k}\Omega$, $\beta_0 = 99$, and $r_o = 46.8\text{ k}\Omega$.



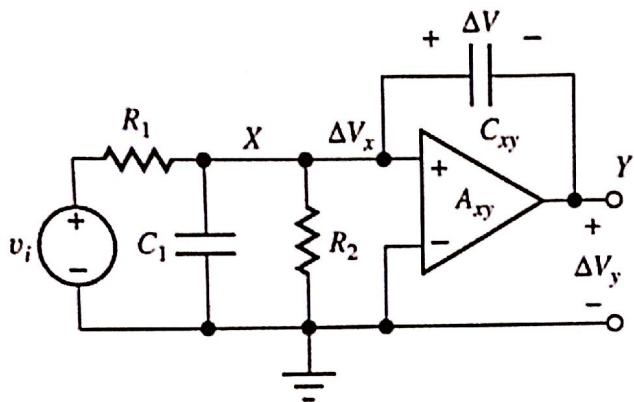
- (1) 2300 rad/s
 (2) 222 rad/s
 (3) 96.1 rad/s
 (4) 4513 rad/s
 ✓ (5) 2.16×10^{-4} rad/s
 (6) None of the above

$$R_{2S} C_2 = 23.3 \times 10 \text{ } \mu\text{s}$$

$$= 0.233 \text{ ms}$$

$$\begin{aligned} \omega_L &= \frac{1}{R_1 s C_1} + \frac{1}{R_{2S} C_2} + \frac{1}{R_3 s C_3} \\ &= \left(\frac{1}{4.52} + \frac{1}{0.233} + \frac{1}{10.2} \right) \times 10^3 \\ &= (0.22 + 4.3 + 0.098) \times 10^3 \\ &= 4618 \text{ rad/s} \end{aligned}$$

- 19) The circuit below highlights the so-called "Miller Effect". Determine the equivalent capacitance at the input and the output of this operational amplifier! Assuming $C_{xy}=1\mu F$, $C_1=1\mu F$ and $A_{xy}=-50$.



- 1) C_{eq} (input)= $1\mu F$ and C_{eq} (output)= $1\mu F$
 ✓ 2) C_{eq} (input)= $52\mu F$ and C_{eq} (output)= $1\mu F$
 3) C_{eq} (input)= $50\mu F$ and C_{eq} (output)= $1\mu F$
 4) C_{eq} (input)= $51\mu F$ and C_{eq} (output)= $51\mu F$
 5) C_{eq} (input)= $52\mu F$ and C_{eq} (output)= $50\mu F$
 6) None of the above

$$\frac{\Delta V_y}{\Delta V_x} = A_{xy} = -50$$

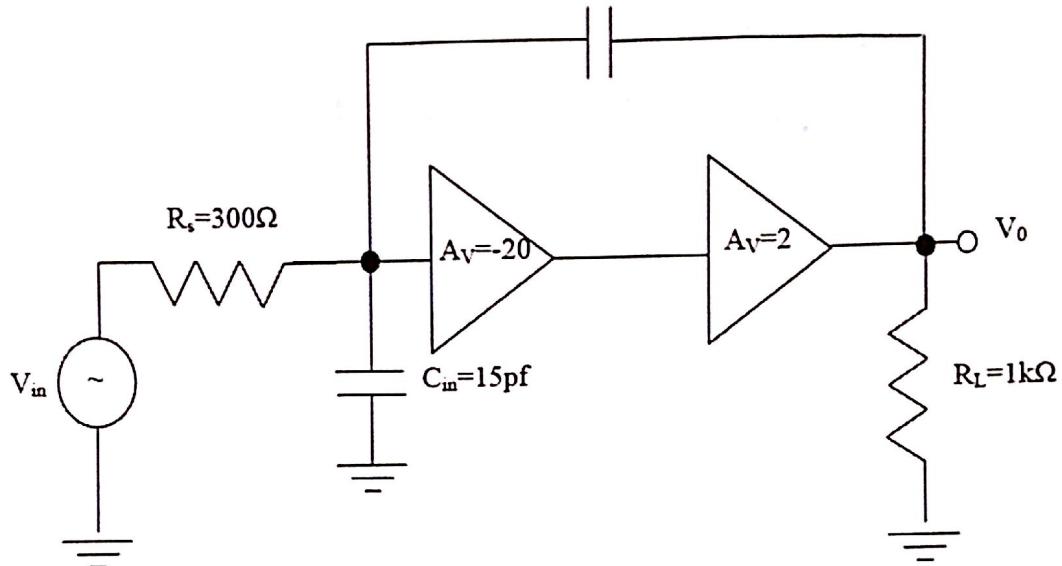
$$C_{xy} \text{ on input side} = C_{xy} (1+50) = 51 C_{xy} = 51 \mu F$$

$$\begin{aligned}
 C_{eq}(\text{input}) &= C_1 + C_{xy\text{in}} \\
 &= 1 \mu F + 51 \mu F \\
 &= 52 \mu F
 \end{aligned}$$

$$C_{xy} \text{ on output side} = C_{xy} (1 + 1/50) \approx 1 \mu F$$

20) For the amplifier circuit shown below what is the high cutoff frequency f_H ?

$$C_f = 0.5 \text{ pF}$$



- (1) ~94 MHz (2) ~5 MHz (3) ~35 MHz \checkmark (4) ~15 MHz
 (5) ~10 MHz (6) None of the above

$$A_{v1} * A_{v2} = A_v = (-20)(2) = -40$$

$$C_{fin} = 0.5(1 + 40) = 41 * 0.5 = 20.5 \text{ pF}$$

$$C_{eq} = C_{eqin} = C_{in} + C_{fin} = 15 + 20.5 = 35.5 \text{ pF}$$

$$R_s C_{eqin} = 300 * 35.5 = 10.65 \text{ ns} \rightarrow \text{dominant pole}$$

$$C_{fout} = 0.5(1 + 1/40) \approx 0.5 \text{ pF}$$

$$R_L C_{fout} = 1000 * 0.5 = 0.5 \text{ ns} \rightarrow 318.5 \text{ MHz}$$

$$\therefore f_H = \frac{1}{2\pi(10.65) \text{ ns}} \approx 15 \text{ MHz}$$