

# High-Performance MoS<sub>2</sub> Field-Effect Transistors Enabled by Chloride Doping: Record Low Contact Resistance (0.5 kΩ-μm) and Record High Drain Current (460 μA/μm)

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## Abstract

In this paper, we report a novel chemical doping technique to reduce the contact resistance ( $R_c$ ) of transition metal dichalcogenides (TMDs) – eliminating two major roadblocks (namely, doping and high  $R_c$ ) towards demonstration of high-performance TMDs field-effect transistors (FETs). By using 1,2 dichloroethane (DCE) as the doping reagent, we demonstrate an active n-type doping density  $> 2 \times 10^{19} \text{ cm}^{-3}$  in a few-layer MoS<sub>2</sub> film. This enabled us to reduce the  $R_c$  value to a record low number of 0.5 kΩ-μm, which is  $\sim 10 \times$  lower than the control sample without doping. The corresponding specific contact resistivity ( $\rho_c$ ) is found to decrease by two orders of magnitude. With such low  $R_c$ , we demonstrate 100 nm channel length ( $L_{ch}$ ) MoS<sub>2</sub> FET with a drain current ( $I_{ds}$ ) of 460 μA/μm at  $V_{ds} = 1.6 \text{ V}$ , which is twice the best value reported so far on MoS<sub>2</sub> FETs.

## Introduction

Semiconducting TMDs possess unique electrical and optical properties due to their d-electron orbitals and 2D nature [1,2]. Among TMDs, MoS<sub>2</sub> has attracted the most attention for its potential applications in low-power electronics [3,4]. However, high  $R_c$  value limits the device performance of MoS<sub>2</sub> FETs significantly and the realization of ohmic contacts for MoS<sub>2</sub> remains a challenge so far [5]. There were several attempts to reduce  $R_c$  including use of low workfunction metal [6] and employing edge contact concept [7]. One of the keys to resolve this issue is to dope the MoS<sub>2</sub> film, however doping the atomically thin film is nontrivial and requires a simple and reliable process technique [8-10]. In this work, we demonstrate such a doping technique enabling high-performance MoS<sub>2</sub> FET.

## Fabrication and Physical Characterization

Fig. 1(a) schematically shows the MoS<sub>2</sub> back-gate FET fabricated in this work. Few-layer MoS<sub>2</sub> flakes were mechanically exfoliated from bulk MoS<sub>2</sub> on a 90 nm SiO<sub>2</sub>/p<sup>+</sup> Si substrate and then soaked in DCE. Acetone and isopropanol rinses were used to remove the residue of the chemical. After e-beam lithography, Ni (30 nm)/Au (60 nm) were deposited to form S/D contacts. The thickness of the MoS<sub>2</sub> flake was identified by the optical image (Fig. 2(a)) and measured by the AFM (Fig. 2(b)). The flake thickness was  $\sim 4 \text{ nm}$ , corresponding to about 6 monolayers. Fig. 2(c) shows an SEM image of a fabricated TLM structure. The presence of Cl in DCE treated MoS<sub>2</sub> film was confirmed by XPS and SIMS, as shown in Fig. 3 (a) and (b). In Fig. 4, we observe a relative blue shift in the binding energies of the core level peaks of the MoS<sub>2</sub> sample that was treated with DCE, which results from an upward shift in the Fermi level, and hence can be attributed to an n-type doping of the sample. However, we note that Cl, when acts as an adatom dopant, results in p-type doping in MoS<sub>2</sub> film [11]. Thus, such n-type doping can be attributed to the donation of extra electron when substitution of S<sup>2-</sup> by Cl takes place, particularly at the sites of sulfur vacancies in the MoS<sub>2</sub> film.

## Contact Resistance Reduction

The TLM resistances of MoS<sub>2</sub> FETs at 50 V back-gate-bias ( $V_{bg}$ ) with and without the Cl doping are plotted as a function of contact separations in Fig. 5(a). The extracted  $R_c$  is significantly reduced from 5.4 kΩ-μm to 0.5 kΩ-μm after the Cl doping. Such an improvement in  $R_c$  is attributed to the doping induced thinning of tunneling barrier width. In Fig. 6, we observe that the extracted  $R_c$  is a weak function of temperature (although the sheet resistance changes by a factor of 2), indicating the dominance of tunneling component of the current over thermionic component at the contact interface. In order to determine the  $\rho_c$ , the transfer lengths ( $L_T$ ) of Ni-MoS<sub>2</sub> junctions are extracted by the TLM and are determined to be

60 nm and 590 nm for the contacts with and without the Cl doping, respectively. Compared with the control sample without the Cl doping, the  $\rho_c$  is reduced from  $3 \times 10^5 \text{ Ω-cm}^2$  to  $3 \times 10^7 \text{ Ω-cm}^2$  when the DCE treatment time is 36 hours, as shown in Fig. 7. The n-type doping concentration ( $N_d$ ) by chloride is  $\sim 2.3 \times 10^{19} \text{ cm}^{-3}$  extracted from the slope of the TLM fitting when  $V_{bg}$  is 0 V. Fig. 8 shows the channel resistance and the  $R_c$  as a function of  $V_{bg}$  for a 1 μm device. Usually in back-gated MoS<sub>2</sub> FETs,  $R_c$  strongly depends on  $V_{bg}$  because  $V_{bg}$  would electrostatically dope the semiconductor underneath the contact, thus reducing the  $R_c$ . In this work, the  $R_c$  shows very weak dependence on  $V_{bg}$  when  $V_{bg}$  is larger than -30 V, indicating heavily doped S/D regions are realized. Since back gate is not necessary for achieving the low  $R_c$  any more, it paves the way to realize three-terminal top-gate low- $R_c$  MoS<sub>2</sub> FETs. The present Cl doping technique with DCE treatment is also valid for the other TMD materials such as WS<sub>2</sub>, whose  $E_f$  is pinned near the middle of the band bandgap.

## Electrical Performance of MoS<sub>2</sub> FET

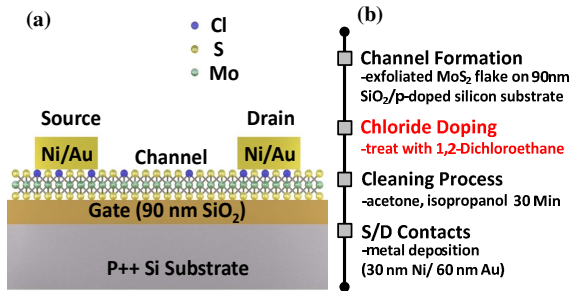
Fig. 9 shows the output characteristics of 100 nm  $L_{ch}$  MoS<sub>2</sub> FETs with and without the Cl doping. The reduced  $R_c$  helps to boost the  $I_{ds}$  from  $\sim 110 \text{ μA/μm}$  to  $460 \text{ μA/μm}$  at  $V_{ds} = 1.6 \text{ V}$ , which is twice of the best reported value so far on MoS<sub>2</sub> FETs at the same  $L_{ch}$  [6]. Fig. 10 shows the components of total resistance ( $R_{total}$ ) indicating mitigation of the adverse dominance of high Schottky S/D contact resistance ( $R_{sd}$ ) at 100 nm  $L_{ch}$ . Such reduction in  $R_{sd}$  also results in excellent current saturation, as observed in Fig. 9. The transfer characteristics of the two devices are shown in Fig. 11. Due to its relatively large bandgap and ultra-thin channel, we achieved an excellent  $I_{on}/I_{off}$  of  $\sim 6.3 \times 10^5$ . Considering the thick gate oxide (90 nm) used in this work, the  $I_{on}/I_{off}$  ratio can be further improved by EOT scaling down. As shown in Fig. 12, the intrinsic long channel field-effect motility ( $\mu_{FE}$ ) as a function of gate electric field is calculated for different  $L_{ch}$  by appropriately eliminating the  $R_{sd}$  effect with a peak  $\mu_{FE}$  of 50-60 cm<sup>2</sup>/Vs. Fig. 13 benchmarks the  $I_{ds}$  ( $V_{ds} = 1.6 \text{ V}$ ) and the  $R_c$  for MoS<sub>2</sub> FETs in literature [5-7, 12-13]. Due to the significant reduction of  $R_c$ , the present work shows superior performance at various  $L_{ch}$  compared with existing literature. These results indicate that the Cl doping by the DCE treatment is an effective way to realize low contact resistance MoS<sub>2</sub> FETs. Table 1 summarizes the electrical performance of the presented devices.

## Conclusion

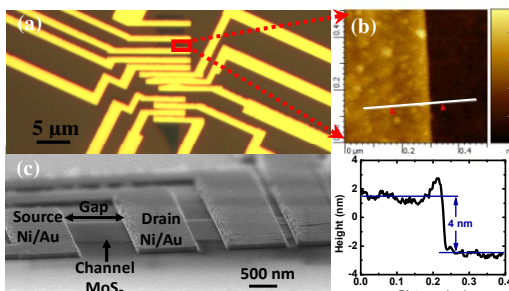
For the first time, a record low  $R_c$  of 0.5 kΩ-μm is achieved on the MoS<sub>2</sub> FET with Cl doping technique. As a result, the  $\rho_c$  significantly decreases from  $3 \times 10^5 \text{ Ω-cm}^2$  to  $3 \times 10^7 \text{ Ω-cm}^2$ . The 100 nm  $L_{ch}$  MoS<sub>2</sub> FETs show a record high  $I_{ds}$  of 460 μA/μm at  $V_{ds} = 1.6 \text{ V}$ , which is twice of the best reported  $I_{ds}$  on any TMD FETs. As a result, this technique is promising for realizing high-performance top-gate low- $R_c$  MoS<sub>2</sub> FETs as well as other TMD based electronic devices.

## References

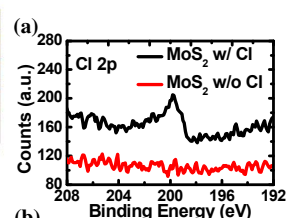
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- [2] H. Wang, *et al.*, *IEDM*, P. 88, 2012 [3] Y. Yoon *et al.*, *Nano Letters*, **11**, p. 3768, 2011 [4] K. Majumdar *et al.*, *EDL*, **35**, p. 402, 2014 [5] H. Liu *et al.*, *ACS Nano*, **8**, p. 1031, 2012 [6] S. Das *et al.*, *Nano Letters*, **13**, p. 3396, 2013 [7] W. Liu, *et al.*, *IEDM*, p. 400, 2013 [8] Y.C. Du *et al.*, *EDL*, **34**, p. 1328, 2013 [9] H. Fang *et al.*, *Nano Letters*, **12**, p. 3788, 2012 [10] H. Fang *et al.*, *Nano Letters*, **13**, p. 1991, 2013 [11] J. Chang, *et al.*, *arXiv: 1305.7162*, 2013 [12] Y.C. Du *et al.*, *EDL* (in press), 2014 [13] J. Lee, *et al.*, *IEDM*, p. 491, 2013



**Fig. 1** (a) Schematic of the MoS<sub>2</sub> back-gate FET fabricated in this work. The gate dielectric is 90 nm SiO<sub>2</sub>. The S/D contact metal is Ni (30 nm)/Au (60 nm). (b) Process flow for the MoS<sub>2</sub> back-gate FETs with the exfoliated MoS<sub>2</sub> flakes.

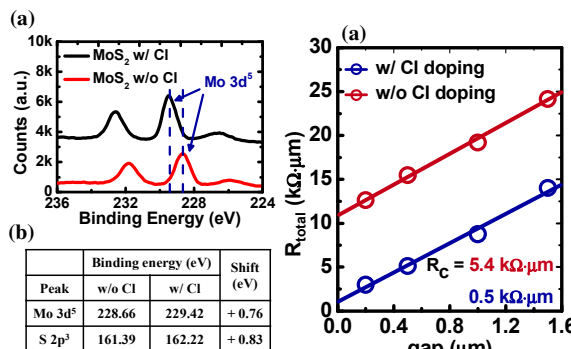


**Fig. 2** (a) Optical image of the few-layer MoS<sub>2</sub> FETs with the Ni/Au contacts on 90 nm SiO<sub>2</sub>/p<sup>++</sup> Si substrate. (b) AFM image of a ~4 nm thick MoS<sub>2</sub> flake and the measured height at the flake edge. (c) SEM image of a MoS<sub>2</sub> TLM structure. The scale bar is 500 nm.

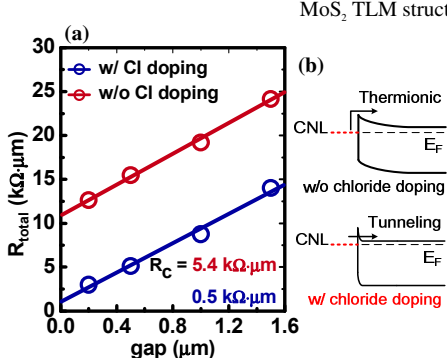


Cl isotope	SIMS count (a. u.)		Ratio
	w/ Cl	w/o Cl	
<sup>35</sup> Cl	2451330	146257	16.76
<sup>37</sup> Cl	750188	33215	22.59

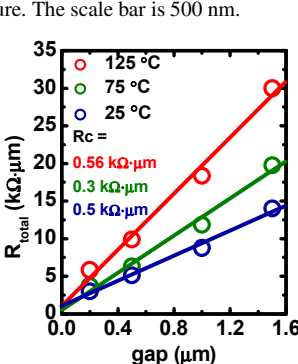
**Fig. 3** Cl signal from MoS<sub>2</sub> after the doping confirmed by (a) XPS and (b) SIMS.



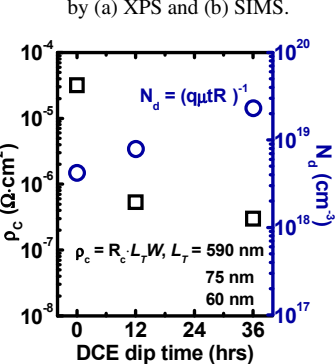
**Fig. 4** (a) XPS spectra of Mo 3d<sub>5</sub> w/ and w/o the Cl doping. A blue shift of 0.76 eV is observed. (b) Binding energy of the core levels w/ and w/o the Cl doping.



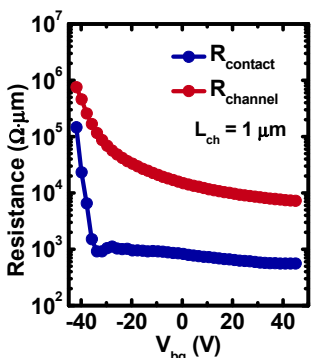
**Fig. 5** (a) TLM resistances of MoS<sub>2</sub> FETs w/ and w/o the Cl doping at V<sub>bg</sub> = 50 V. The R<sub>c</sub> is reduced from 5.4 kΩ·μm to 0.5 kΩ·μm. (b) Band diagram of the metal-MoS<sub>2</sub> contacts w/ and w/o the Cl doping. R<sub>c</sub> is reduced due to the doping induced thinning of tunneling barrier width.



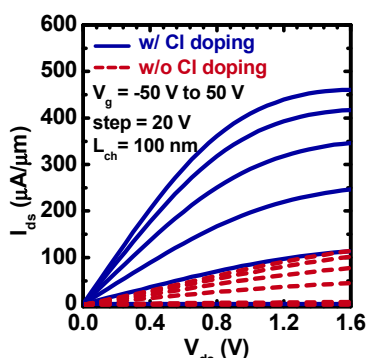
**Fig. 6** TLM resistances of MoS<sub>2</sub> FETs at 25, 75, and 125 °C. The extracted R<sub>c</sub> remains similar from 25 °C to 125 °C, indicating the current is dominated by tunneling.



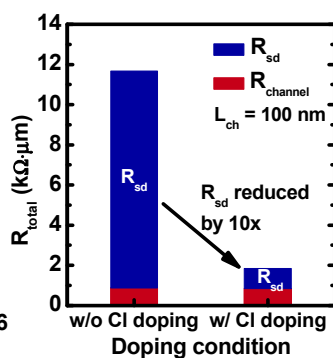
**Fig. 7** DCE dip time dependence of ρ<sub>c</sub> and N<sub>d</sub> with the DCE treatment. The ρ<sub>c</sub> is reduced by 100 after a 36 hours DCE treatment. N-type doping density of 2×10<sup>19</sup> cm<sup>-3</sup> is indicated.



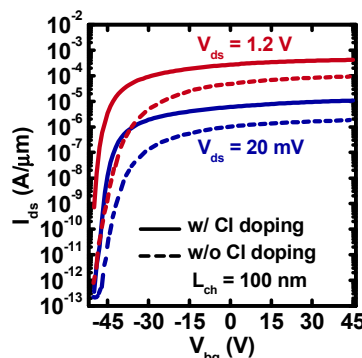
**Fig. 8** R<sub>channel</sub> and R<sub>contact</sub> vs. V<sub>bg</sub> for the 1 μm device. The R<sub>contact</sub> shows very weak dependence on the V<sub>bg</sub> when V<sub>bg</sub> > -30 V indicating heavily doped contact regions.



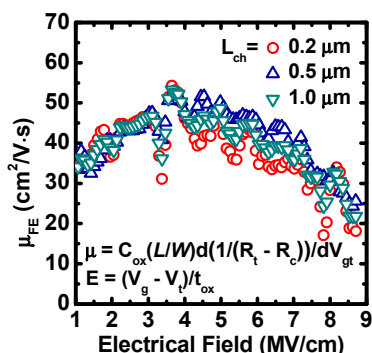
**Fig. 9** Output characteristics of the 100 nm L<sub>ch</sub> MoS<sub>2</sub> FETs w/ and w/o the Cl doping. A record high I<sub>ds</sub> of 460 μA/μm is obtained.



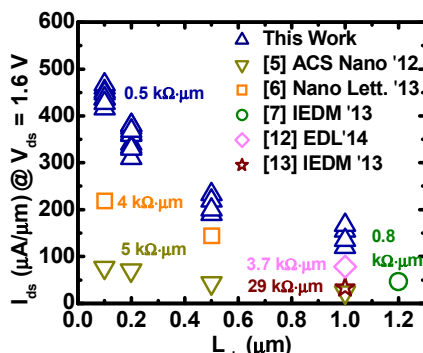
**Fig. 10** Component of R<sub>total</sub> for the two devices. The R<sub>total</sub> of 100 nm L<sub>ch</sub> MoS<sub>2</sub> FET is reduced from 11.7 kΩ·μm to 1.85 kΩ·μm due to the Cl doping.



**Fig. 11** Transfer characteristic curves of the 100 nm L<sub>ch</sub> MoS<sub>2</sub> FETs w/ and w/o the Cl doping. The I<sub>on</sub>/I<sub>off</sub> ratio is 6.3×10<sup>5</sup> at V<sub>ds</sub> = 1.2 V.



**Fig. 12** Calculated intrinsic field-effect motilities as a function of the gate field for the MoS<sub>2</sub> FET with various L<sub>ch</sub>.



**Fig. 13** Benchmarking of the I<sub>ds</sub> @ V<sub>ds</sub> = 1.6 V and the R<sub>c</sub> in the reported MoS<sub>2</sub> back-gate FETs.

Summary of present work	
EOT	90 nm
L <sub>ch</sub>	100 nm
R <sub>c</sub>	0.5 kΩ·μm
ρ <sub>c</sub>	3×10 <sup>-7</sup> Ω·cm <sup>2</sup>
I <sub>ds</sub>	460 μA/μm @ 1.6 V
I <sub>on</sub> /I <sub>off</sub>	> 6.3×10 <sup>5</sup> @ 1.2V
max. μ <sub>FE</sub>	50-60 cm <sup>2</sup> /V·S

**Table. 1** Summary of the electrical performance of the MoS<sub>2</sub> FETs in this work.

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